

**FIB/RIE METHOD FOR IN-LINE CIRCUIT MODIFICATION OF
MICROELECTRONIC CHIPS CONTAINING ORGANIC DIELECTRIC**

ABSTRACT

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A method for circuit modification of an microelectronic chip having at least one conductor in an organic dielectric, includes applying a protective inorganic surface layer on top of the organic dielectric, forming at least one window in the protective inorganic surface layer to selectively expose the underlying organic dielectric, etching the organic dielectric in the window area to selectively remove the organic dielectric adjacent to the conductor, and performing at least one process that modifies the conductor.